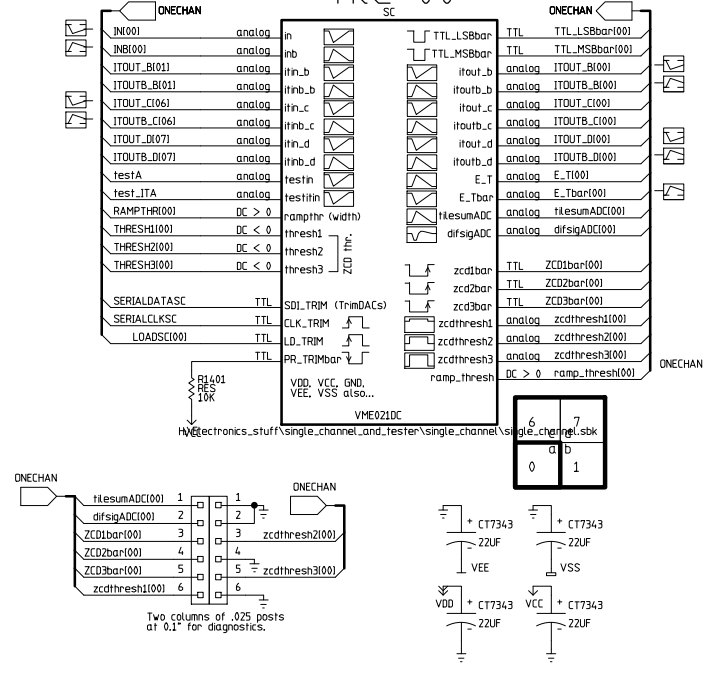
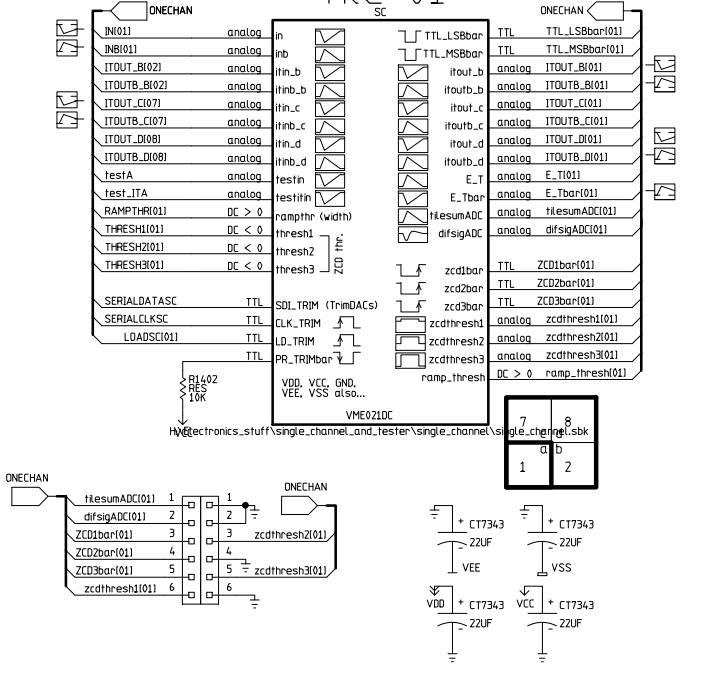


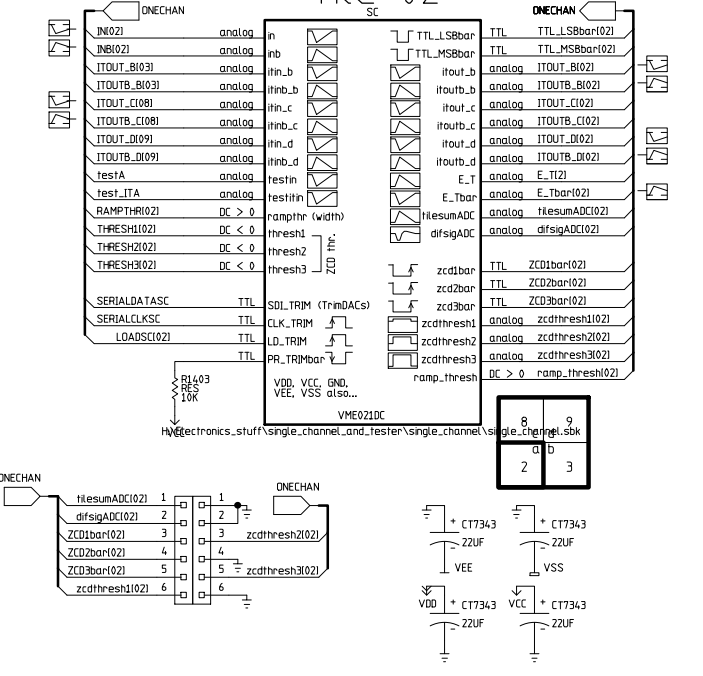
Tile 00



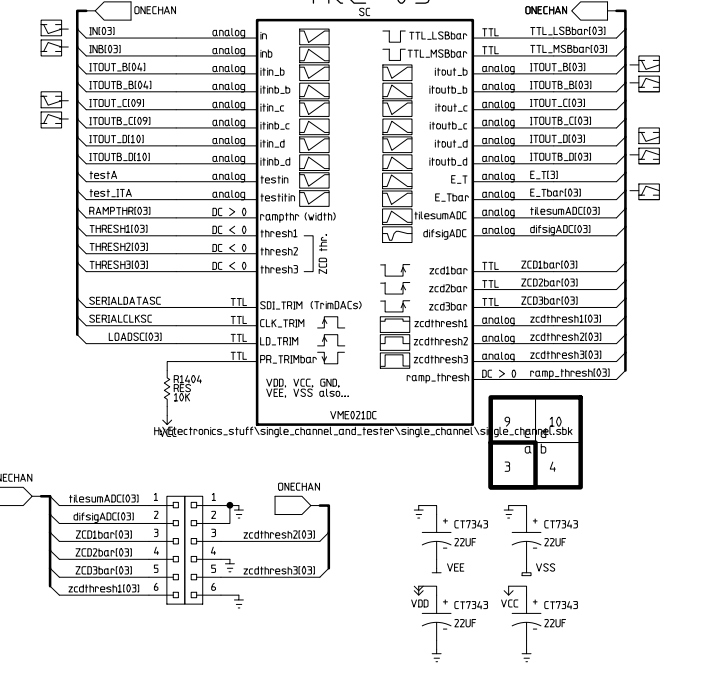
Tile 01



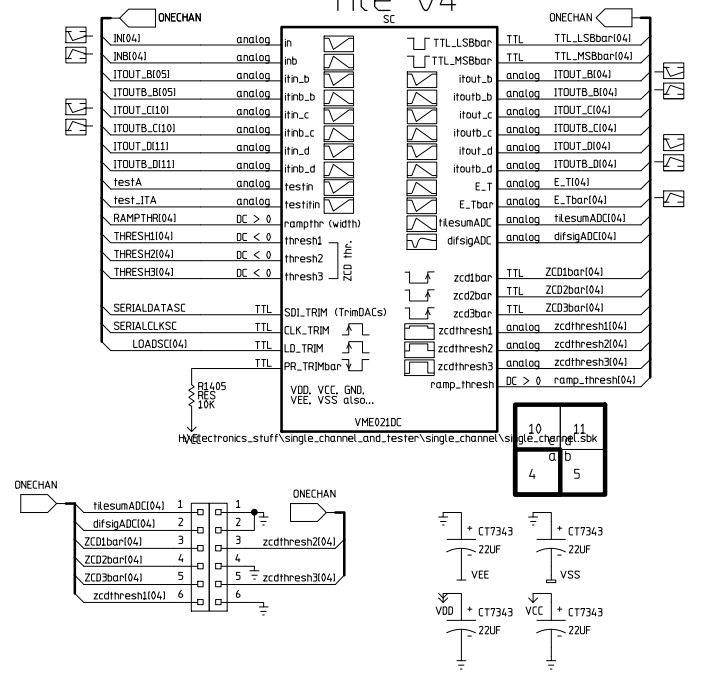
Tile 02



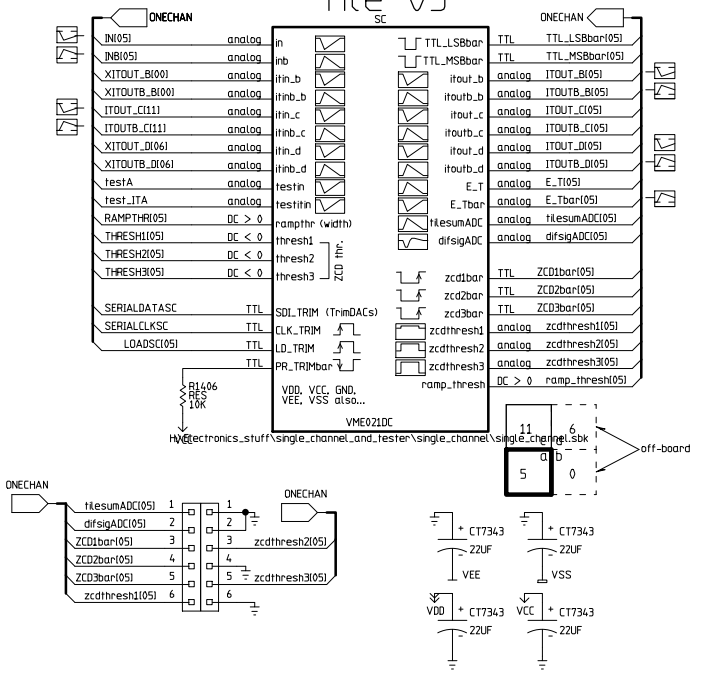
Tile 03



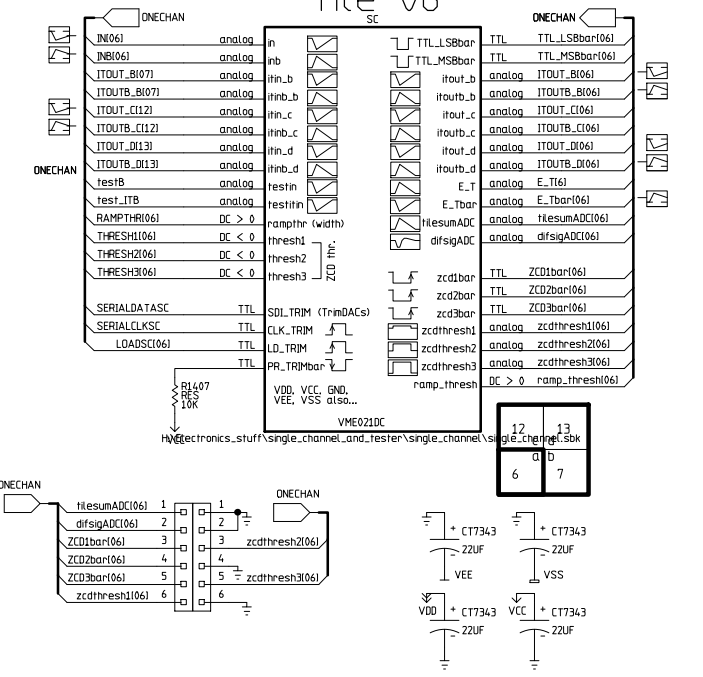
Tile 04



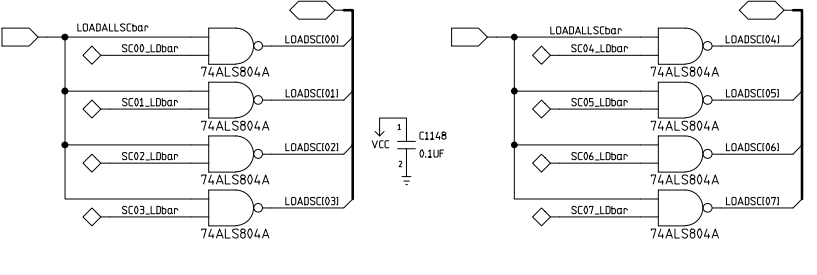
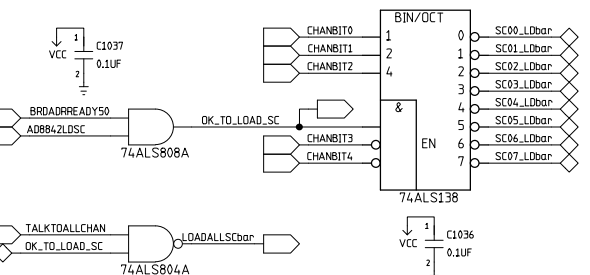
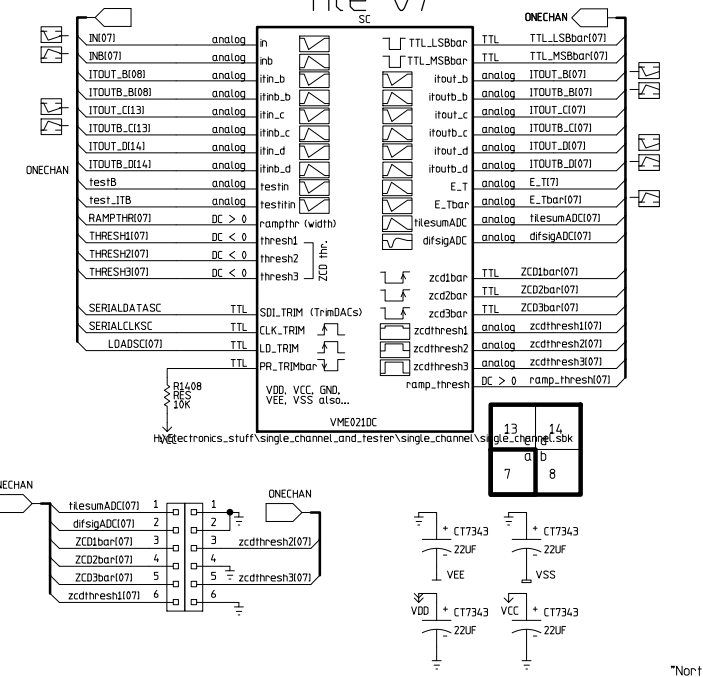
Tile 05



Tile 06



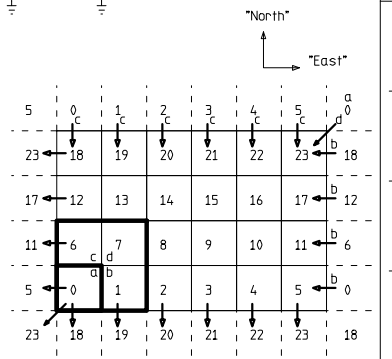
Tile 07



24 mixer/shaper cards per tiling card.
 Indicated: signal export/import across tiling card boundaries.
 c outputs are fed "due south"
 b outputs are fed "due west"
 d outputs are fed "southwest"
 c inputs come from "due north"
 b inputs come from "due east"
 d inputs come from "northeast"

The tile rooted in m/s card 0 receives inputs from the following m/s cards:
 a input from m/s 0,
 b input from m/s 1,
 c input from m/s 6,
 d input from m/s 7.

The tile rooted in m/s card 23 receives inputs from the following m/s cards:
 a input from m/s 18 in the "one-to-the-north" tiling card,
 b input from m/s 0 in the "one-to-the-northeast" tiling card,
 c input from m/s 5 in the "one-to-the-east" tiling card,
 d input from m/s 23.



Tile 16

Tile 17

Tile 18

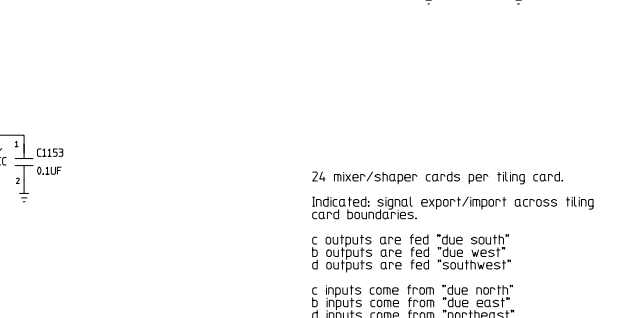
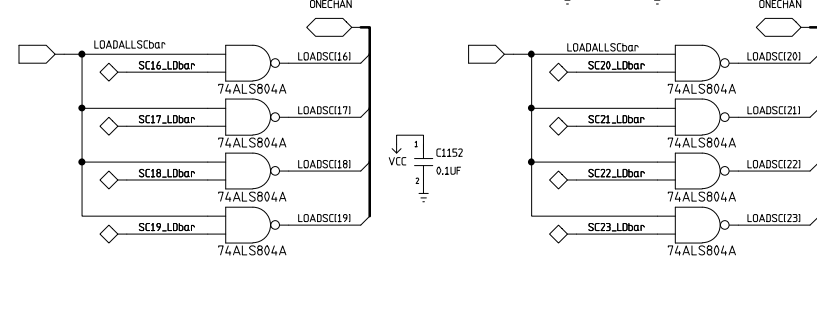
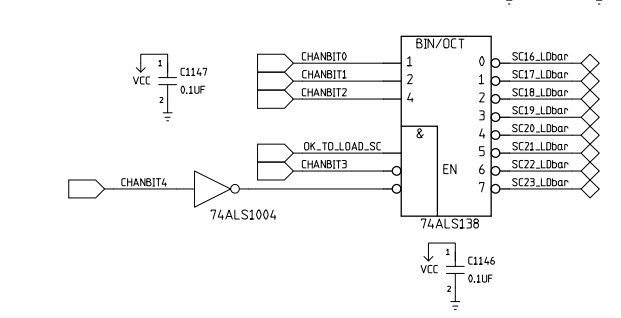
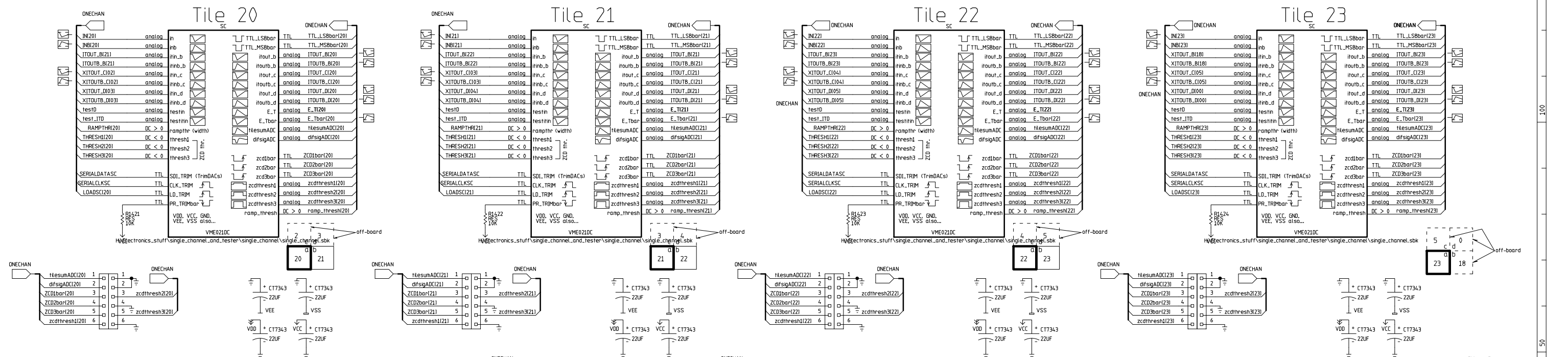
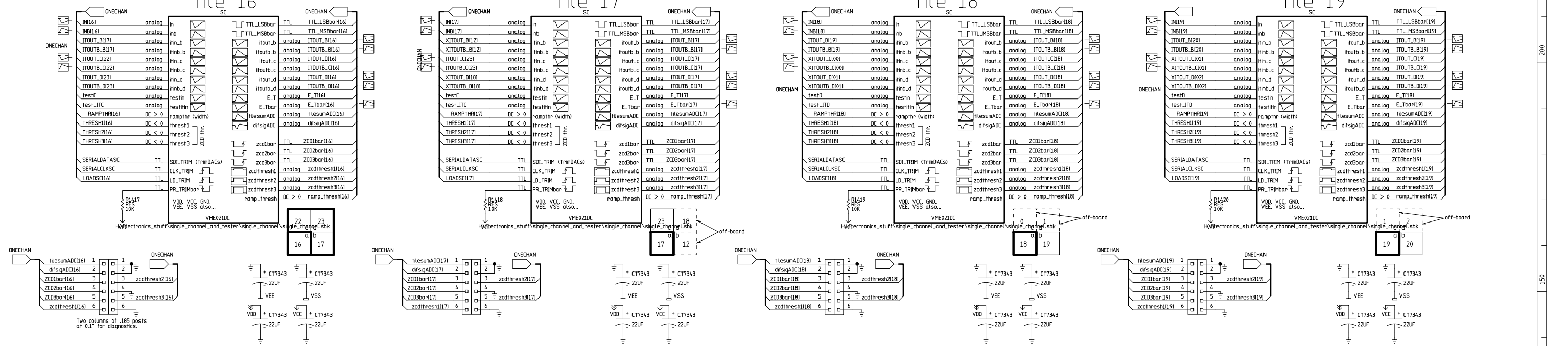
Tile 19

Tile 20

Tile 21

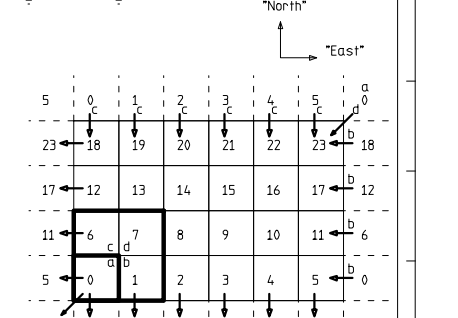
Tile 22

Tile 23



24 mixer/shaper cards per tiling card.
 Indicated: signal export/import across tiling card boundaries.
 c outputs are fed "due south"
 b outputs are fed "due west"
 d outputs are fed "southwest"
 c inputs come from "due north"
 b inputs come from "due east"
 d inputs come from "northeast"

The tile rooted in m/s card 0 receives inputs from the following m/s cards:
 a input from m/s 0,
 b input from m/s 1,
 c input from m/s 6,
 d input from m/s 7.
 The tile rooted in m/s card 23 receives inputs from the following m/s cards:
 c input from m/s 5 in the "one-to-the-north" tiling card,
 a input from m/s 0 in the "one-to-the-northeast" tiling card,
 b input from m/s 18 in the "one-to-the-east" tiling card, and
 a input from m/s 23.



Convert TTL to low-voltage-differential-signals to send to tile processor.

