Receive mixer/shaper differential signal and fan it out.

Use GND as the return path for VCC, VEE current.
Use AGND as the return for VDD, VSS. In the version of the PCB, GND and AGND share a single (undivided) ground plane.

Receive test pulse signals, pass through TR1027. Also treats other gain-adjusted signals with TR1040.
Make a gain-adjusted copy of the interline "0" signal, feed this to the sum circuitry.

Make a gain-adjusted copy of the interline "0" signal, feed this to the sum circuitry.
Make a gain-calibrated copy of the interface "C" signal, feed this to the sun circuitry.
NOTE: remove the nested CCT name "SN74LS123" property from the 74LS123s before doing PCB layout.

Generate Gray codes here (NOT binary!!)

[Diagram of circuit components and labels]